



3764

PATENT
Atty. Dkt. AMAT/7650/ETCH/SILICON/JB

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Wej Lui

Serial No.: ~~10/264,763~~

Confirmation No.: Unknown

Filed: July 21, 2003

For: METHOD FOR FABRICATING A
NOTCH GATE STRUCTURE OF A
FIELD EFFECT TRANSISTOR

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Group Art Unit: Unknown

Examiner: ~~Unassigned~~

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TECHNOLOGY CENTER
Toledo, Fernando

CERTIFICATE OF MAILING

37 CFR 1.8

I hereby certify that this correspondence is being deposited on
December 12, 2003 with the United States Postal Service as First
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12/12/03

Date

Signature

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

The Applicants, and the Attorney who signs below on the basis of the information supplied by the inventor and the information in his file, submit herewith patents, publications, or other information of which they are aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

While the information submitted in this Supplemental Information Disclosure Statement may be material pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication, or other information referred to therein is prior art for this invention unless specifically designated as such.

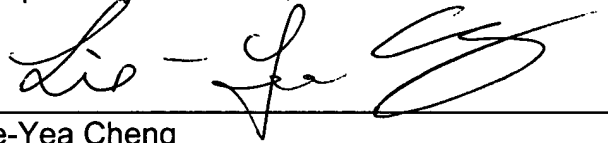
In accordance with 37 CFR § 1.97, this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possibly material information as defined under 37 CFR § 1.56(a) exists.

The patents and/or publications submitted herewith are set forth on the attached Form PTO-1449.

Applicants certify that all references submitted with this disclosure were cited in a communication from a foreign patent office dated November 12, 2003, which communication is enclosed, not more than three months prior to the filing of this Supplemental Information Disclosure Statement.

If the sum of \$180.00 is due under 37 CFR § 1.17(p) pursuant to § 1.97, the Commissioner is hereby authorized to charge this fee, and any other fee necessary to make this submission timely, to the Deposit Account No.: 20-0782/AMAT/7650/LYC.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Lie-Yea Cheng', is written over a horizontal line.

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U.S. Department of Commerce, Patent and Trademark Office		Docket No.	Serial No.
(PTO Form 1449 modified)		AMAT/7650/ETCH/SILICON/JB	10/264,763
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Applicant Wei Liu	Confirmation No. Unknown
(Use several sheets if necessary)		Filing Date July 21, 2003	Group Unknown
Examiner	Unassigned		

**U.S. Patent Documents**

*Examiner Initial		Document Number	Issue Date	Applicant(s) Name	Class	Subclass	Filing Date If Appropriate
	A1	5,563,079	10/08/1996	Shin, et al.	438	571	
	A2	5,981,398	11/9/1999	Tsai, et al.	438	710	
	A3	6,207,490	3/27/2001	Lee	438	241	
	A4	6,255,202	7/03/2001	Lyons, et al.	438	585	
	A5	2003/143791	7/31/2003	Kang, et al.			
	A6						
	A7						
	A8						
	A9						
	A10						
	A11						
	A12						
	A13						

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Foreign Patent Documents

*Examiner Initial		Document Number	Date	Country	Class	Subclass	Translation	
							YES	NO
	B1	EP0777269 A2	6/04/1997				<input type="checkbox"/>	<input type="checkbox"/>
	B2	EP0777269 A3	2/03/1999				<input type="checkbox"/>	<input type="checkbox"/>
	B3						<input type="checkbox"/>	<input type="checkbox"/>
	B4						<input type="checkbox"/>	<input type="checkbox"/>
	B5						<input type="checkbox"/>	<input type="checkbox"/>

OTHER ART

*Examiner Initial		Including Author, Title, Date, Pertinent Pages, Etc.
	C1	
	C2	
	C3	

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.